ABSTRACT OF THE DISCLOSURE

An active matrix LCD defines a display signal period and a reset period in each vertical scan period. The display signal period is a period to write and hold display signals in pixels in response to a row select pulse generated from an output pulse of a first shift register (SR1). The reset period is a period to write and hold a reset voltage in the pixels in response to a row select pulse generated from an output pulse of a second shift register (SR2). The ratio of the display signal period to the reset period is adjustable in units of horizontal scan time by changing the number "n" of horizontal scan periods to be passed between the time when the first shift register receives a scan start signal (WT) and the time when the second shift register receives a scan start signal (Reset).